



4th International Symposium on
Energy **C**hallenges & **M**echanics
- working on small scales

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Invited Speaker of Session 20

CRYSTAL GROWTH AND ITS ENERGY AND ENVIRONMENTAL APPLICATIONS



Tatsuro Maeda is Senior Research Scientist in National Institute of Industrial Science and Technology (AIST), Japan. He received the Ph.D. degree in material science from Tokyo Institute of Technology, Tokyo, in 1996. In 1996, he joined the Electron Device Division, Electrotechnical Laboratory, Ibaraki, Japan where he has been engaged in the research on fabrication and characterization of ultrasmall SOI-MOSFETs, nano-scale Si-related devices, and single electron transistors for future ULSIs. In 2001, he joined the MIRAI Project, Association of Super-Advanced Electronics Technology (ASET) and Advanced Semiconductor Research Center-National Institute of Industrial Science and Technology (ASRC-AIST), Ibaraki, Japan, where he worked as a Member of New Transistor Structures and Measurement/Analysis Technology Group, developing stained-Si MOSFETs. In 2007-2009, he worked at Japan External Trade Organization (JETRO), San Francisco Center. Now he belongs to

Nanoelectronics Research Institute in AIST. His current research interests include advanced research on post-silicon devices such as stained Si, and Ge III-V materials, ranging from device physics, fabrication nanotechnology, as well as physical and electrical characterization. Dr. Maeda is a member of the Japan Society of Applied Physics.

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