



4th International Symposium on
Energy **C**hallenges & **M**echanics
- working on small scales

11-13 August 2015
Aberdeen, Scotland, UK

Speaker of Session 20

CRYSTAL GROWTH AND ITS ENERGY AND ENVIRONMENTAL APPLICATIONS



NAME: Cong Wang

Date of Birth: April 14, 1982

Place of Birth: Qingdao, Shandong Province, China

Address: Rm#202, 236-32, Sindang 1-dong, Jung-gu,
Seoul, Korea

Dr. Cong Wang was born in Qingdao, Shandong Province, China in 1982. He received the B.S. degree in Automation Engineering from Qingdao Technological University (China) in 2005, the M.S. and Ph. D. degrees in Electronic Engineering from Kwangwoon University (Korea) in 2008 and 2011 respectively. He is currently working at the same university as an assistant professor from Feb. 2013. In

2007, he was a research engineer at Mission Technology Co., Ltd R&D Department, Gyeonggi-do, Korea, where he was involved in the development of microwave components and devices using Hybrid and LTCC technology. He joined NanoENS Co., Ltd R&D Department that same year. His work includes passive device modeling, passive device design, and fabrication process development and optimization. He is a member of the Korea Institute of Electromagnetic Engineering and Science (KIEES), the Korea Information and Communications Society (KICS), the Korean Institute of Electrical and Electronic Material Engineers (KIEEME), the Japan Institute of Electronics, the Information and Communication Engineers (IEICE), and a student member of the U. S. Institute of Electrical and Electronics Engineers (IEEE). During his Master and Ph.D. courses, he was involved in many projects and assisted in teaching for undergraduate and graduate students. He has worked as an adjunct lecturer (2010) and a research professor (2011-2013) at Kwangwoon University. He has also published more than 150 papers in domestic and international journals and conferences. He is listed in 'Marquis Who's Who in the World, (2010-2014)' for his outstanding contribution in his own field as a researcher and educator. He also has a few semiconductor layout designs and patents registered in Korea. His major interests include RFIC/MMIC design and semiconductor fabrication development such as GaAs integrated passive device, SU-8 photo resist based fabrication, silicon-based LED module fabrication and packaging, and AlGaIn/GaN HEMT and their applications which are emerging technologies of today.

