

Novel simulation methods for investigations on distributed power generation

Georg Lauss*, Felix Lehfuss

AIT Austrian Institute of Technology, Vienna 1220, Austria

Accepted for publication on the 25th of January 2015

This work presents a compendious overview over Power Hardware-in-the-Loop (PHIL) simulations that are used for designing, analysing and testing of electrical power system and its components. PHIL simulations are an advanced application of real time simulations that represent novel methods which conjoins soft- and hardware simulation / testing. It briefly outlines necessary requirements for the implementation of PHIL simulations, which are defined by the nature of the digital real time simulator (DRTS), the power amplifier (PA) and the Power Interface (PI). Fundamental characteristics such as the input/output (IO) systems, interface algorithm (IA) and system stability considerations are discussed for PHIL systems in order to illustrate both flexibility and complexity of this compound simulation method. Superior to ordinary offline simulation methods (numeric solvers) or classic laboratory testing the PHIL simulation method is able to extend the application range of investigations on electric system such as distributed power generations. Benefits and disadvantageous issues of PHIL simulations are heavily depending on the system topology and the relevant use case(s) set variously in its origin. Important issues that are part of ongoing discussions can be simulated, modelled and run in this novel simulation method PHIL. The focus is set to experimental results, its verification and useable statements or solutions valuable for both industry and network operator can be achieved exemplarily for the following areas such as (a) Islanding detection scenarios (unintentional islanding), (b) Grid fault scenarios (LVRT, realistic vs. normative grid faults), (c) Integration of communication techniques (Co-simulation, communication protocols), (d) Advanced controls features of the latest generation (P/Q controls, etc.) or (e) System stability behaviour of multiple generation units connected (interactions).

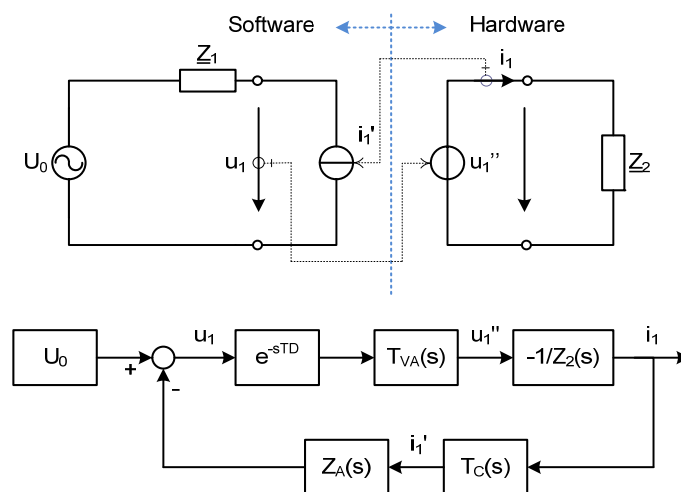


Fig. 1. Sketch of components and interfacing of real software/hardware power interface (voltage type) of a PHIL simulation

Keywords: Simulation methods, Distributed power generation, Hardware-in-the-loop method, Integration of renewables.

References:

- [1] J. Langston, K. Schoder, M. Steurer, O. Faruque, J. Hauer, F. Bogdan, R. Bravo, B. Mather, and F. Katiraei, "Power hardware-in-the-loop testing of a 500 kW photovoltaic array inverter", *Proc. IECON 2012 – 38th Annual Conference on IEEE Industrial Electronics Society*, Montreal, 2012.
- [2] A. Viehweider, G. Lauss, F. Lehfuss, "Interface and Stability Issues for SISO and MIMO Power Hardware in the Loop Simulation of Distribution Networks with Photovoltaic Generation", *International Journal of Renewable Energy Research*, vol. 2, no. 4, 2012.
- [3] A.J. Roscoe; A Mackay; G.M. Burt; J.R. McDonald, "Architecture of a Network-in-the-Loop Environment for Characterizing AC Power-System Behavior," *IEEE Transactions on Industrial Electronics*, vol.57, no.4, pp.1245-1253, April 2010.
- [4] G. Lauss, B. Bletterie, C. Mayr, F. Lehfuss, R. Bründlinger, "Power-Hardware-in-the-Loop Simulations for Electrical Generators in LV Grids", *DER-Journal*, vol.1, no. 9, 2011.
- [5] A.J. Roscoe, I.M. Elders, J.E Hill, G.M. Burt, "Integration of a mean-torque diesel engine model into a hardware-in-the-loop shipboard network simulation using lambda tuning" *Electrical Systems in Transportation, IET* , vol.1, no.3, pp.103-110, September 2011.